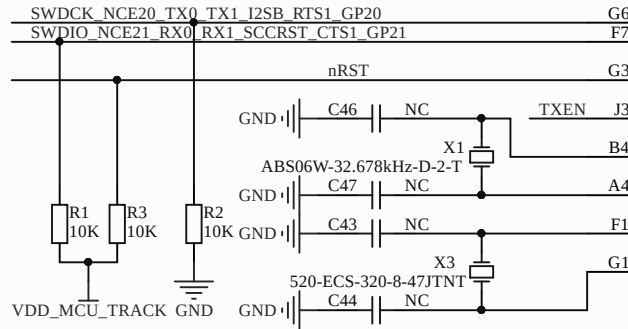


U1

SSCL SSCK CKO MSP14 NCE0 GP0	H4
SSDA SMOSI TX0 MSP15 NCE1 GP1	G4
RX1 SMISO RX0 MSP16 NCE2 GP2	F4
RTS0 SCE NCE3 MSP17 TRG1 I2SW GP3	G5
CTS0 SINT NCE4 RX1 CT17 MISP2 GP4	D9
M0SCL M0SCK RTS0 EXHFA CT8 GP5	E9
M0SDA M0MISO CTS0 CT10 I2SD GP6	E8
NCE7 M0MOSI CKO TRG0 TX0 CT19 GP7	F9
M1SCL M1SCK NCE8 SCCCLK TX1 GP8	F8
M1SDA M1MISO NCE9 SCCIO RX1 GP9	G7
TX1 M1MOSI NCE10 PDMC RTS1 GP10	G8
ADCSE2 NCE11 CT31 SINT CTS1 RX0 PDMD GP11	B5
ADCONSE9 NCE12 CT0 SCE PDMC CTS0 TX1 GP12	A7
ADCOPSE8 NCE13 CT2 I2SB EXHFB RTS0 RX1 GP13	B7
ADC1P NCE14 TX1 PDMC EXHFS SWDCK 32K GP14	D7
ADC1N NCE15 RX1 PDMD EXXT SWDIO SWO GP15	C7
ADCSE0 NCE16 TRG0 SCCRST CMP10 TX0 RTS1 GP16	D4
CMPR1 NCE17 TRG1 SCCCLK RX0 CTS1 GP17	D3
CMP11 NCE18 CT4 RTS0 ATST2 TX1 SCCIO GP18	C5
CMPR0 NCE19 CT6 SCCCLK ATST1 RX1 I2SB GP19	C3
TX0 NCE22 CT12 PDMC EXLF MSP10 SWO GP22	C9
RX0 NCE23 CT14 I2SW CMP0 MSP13 EXXT GP23	D8
TX1 NCE24 MSP18 CTS0 CT21 32K SWO GP24	C8
RX1 NCE25 CT1 M2SDA M2MISO GP25	A8
EXHF NCE26 CT3 SCCRST MSP11 TX0 CTS1 GP26	B8
RX0 NCE27 CT5 M2SCL M2SCK GP27	B9
I2SW NCE28 CT7 M2MOSI TX0 GP28	A9
ADCSE1 NCE29 CT9 CTS0 CTS1 RX0 PDMD GP29	A5
ATST1 NCE30 CT11 TX0 RTS1 BLESCK I2SD GP30	F6
ADCSE3 NCE31 CT13 RX0 SCCCLK BLEMISO RTS1 GP31	D5
ADCSE4 NCE32 CT15 SCCIO EXLF BLEMOSI CTS1 GP32	E6
ADCSE5 NCE33 32K BLECSN CTS0 CT23 SWO GP33	B6
ADCSE6 NCE34 RTS1 CMPR2 RTS0 RX0 PDMD GP34	C6
ADCSE7 NCE35 TX1 I2SD CT27 RTS0 BLESTATUS GP35	D6
TRG1 NCE36 RX1 32K CTS1 CTS0 PDMD GP36	H6
TRG2 NCE37 RTS0 SCCIO TX1 PDMC CT29 GP37	H7
TRIG3 NCE38 CTS0 M3MOSI RX1 GP38	J6
TX0 TX1 CT25 M4SCL M4SCK GP39	J8
RX0 RX1 TRG0 M4SDA M4MISO GP40	J9
NCE41 BLEIRQ SWO I2SW RTS1 TX0 RTS0 GP41	E5
TX1 NCE42 CT16 M3SCL M3SCK GP42	H5
RX1 NCE43 CT18 M3SDA M3MISO GP43	J5
RTS1 NCE44 CT20 M4MOSI TX0 GP44	J7
CST1 NCE45 CT22 I2SD PDMD RX0 SWO GP45	F5
I2SB NCE46 CT24 SCCRST PDMC TX1 SWO GP46	E7
32K NCE47 CT26 M5MOSI RX1 GP47	H9
TX0 NCE48 CT28 M5SCL M5SCK GP48	G9
RX0 NCE49 CT30 M5SDA M5MISO GP49	H8



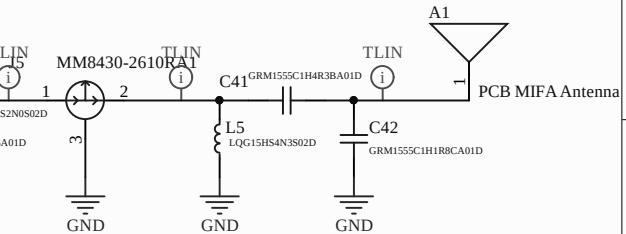
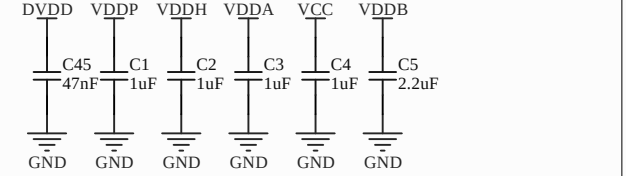
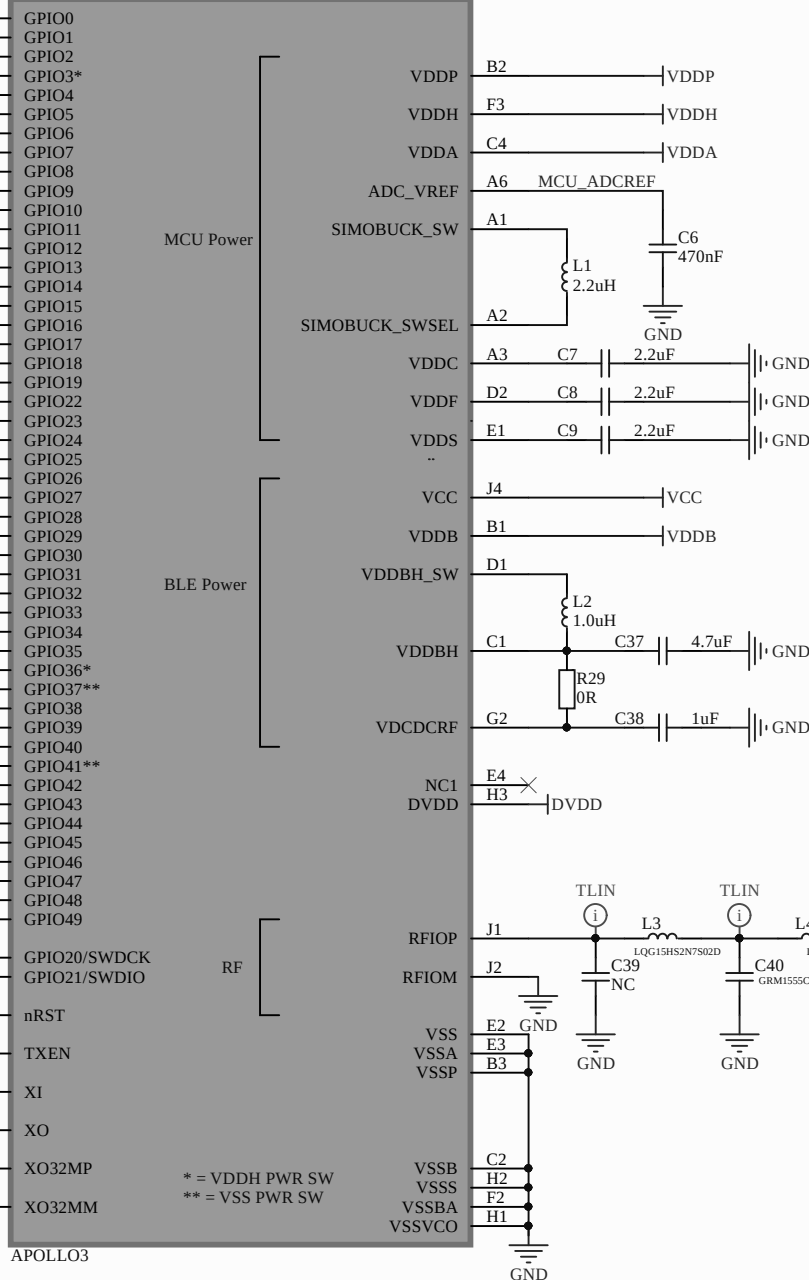
MCU Power

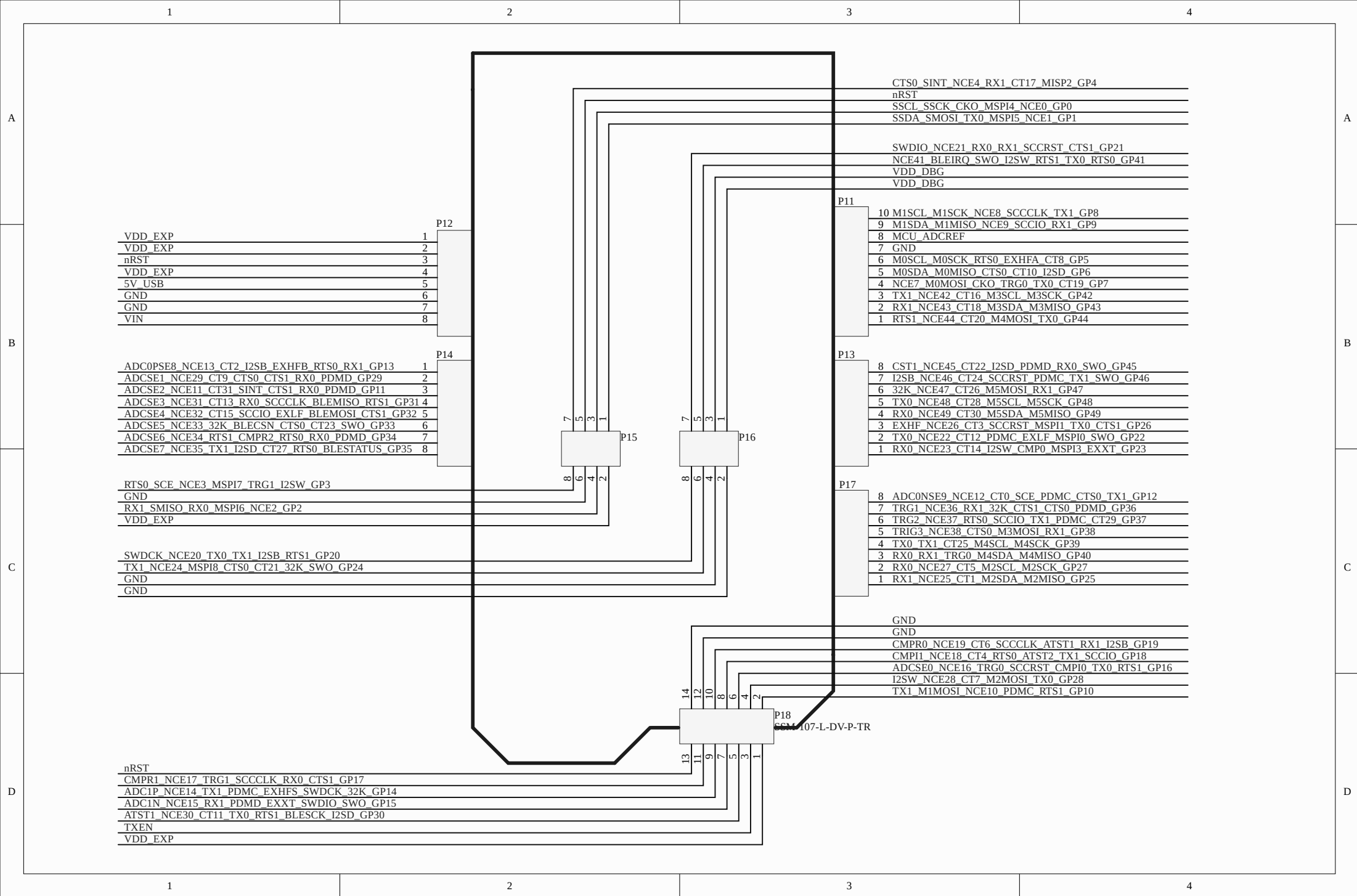
BLE Power

RF

* = VDDH PWR SW
** = VSS PWR SW

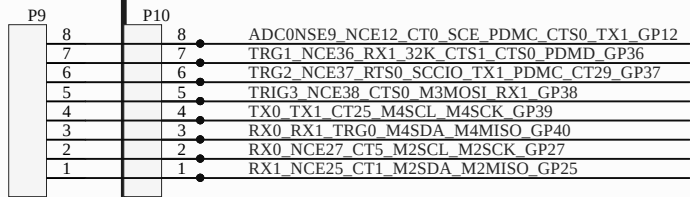
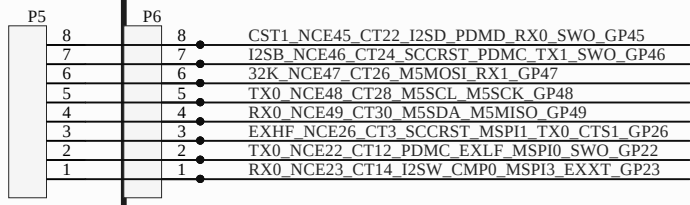
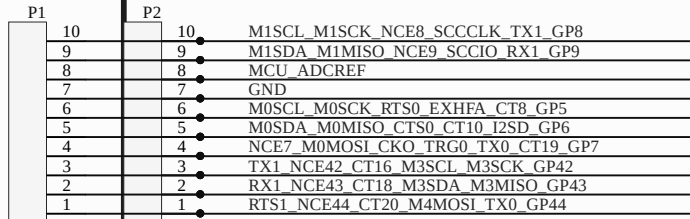
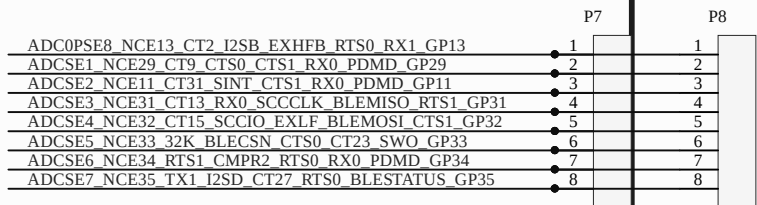
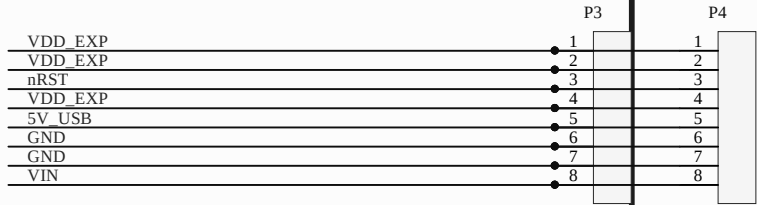
APOLLO3





A

A



B

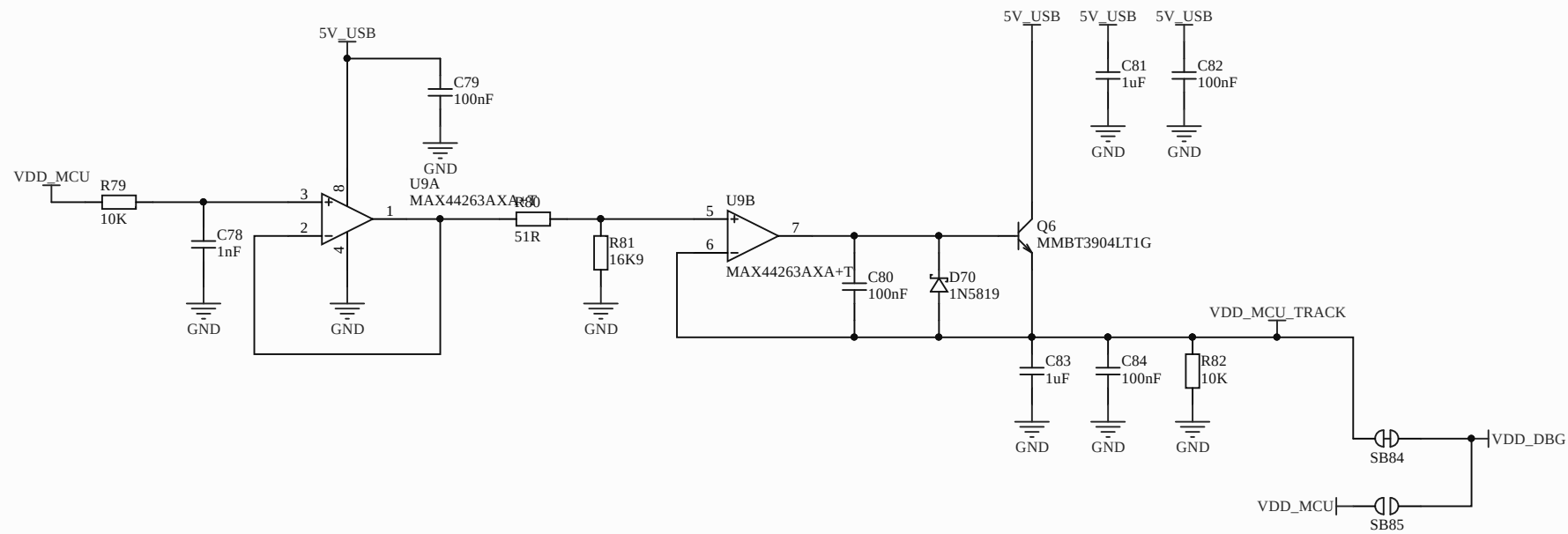
B

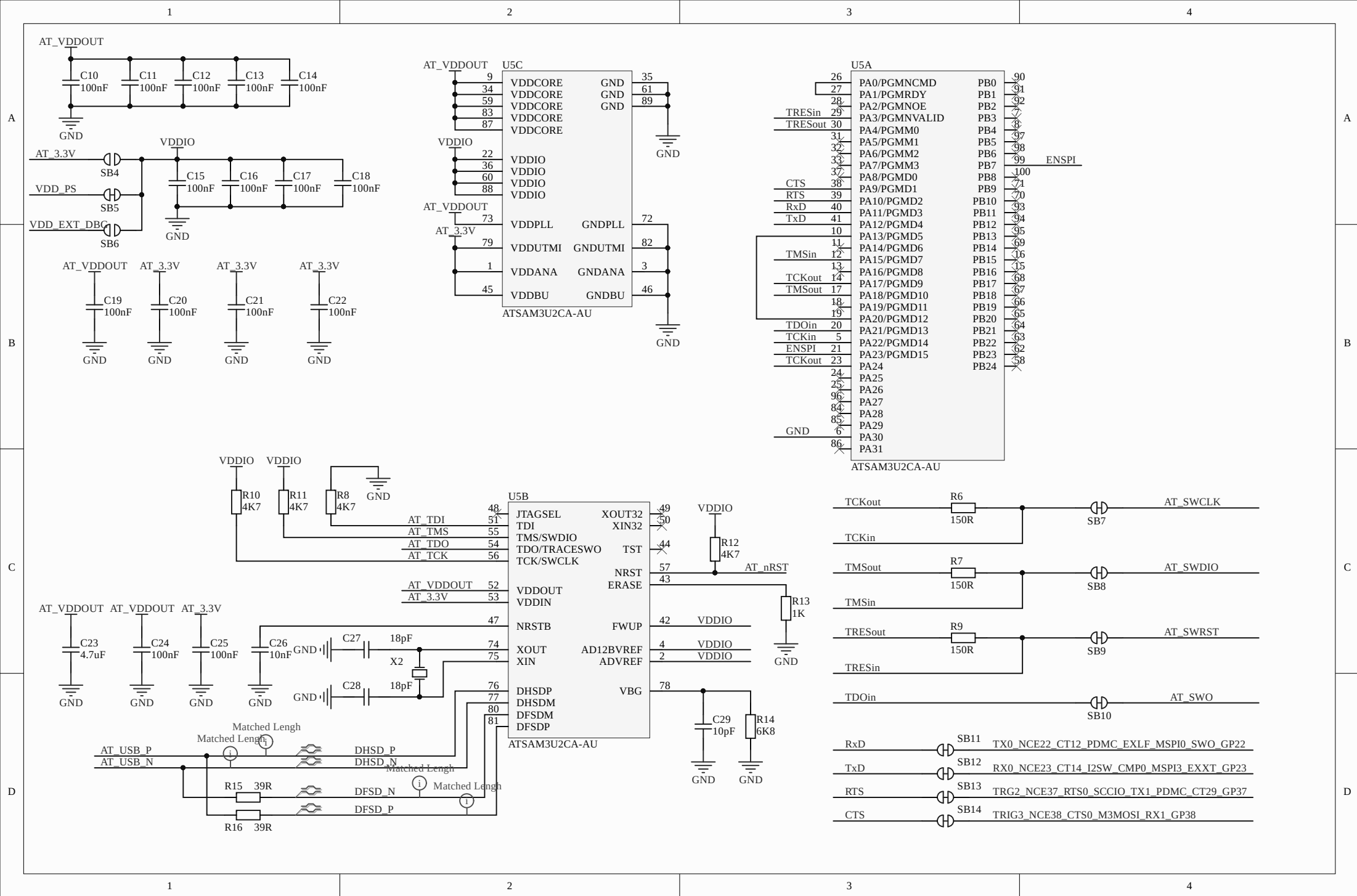
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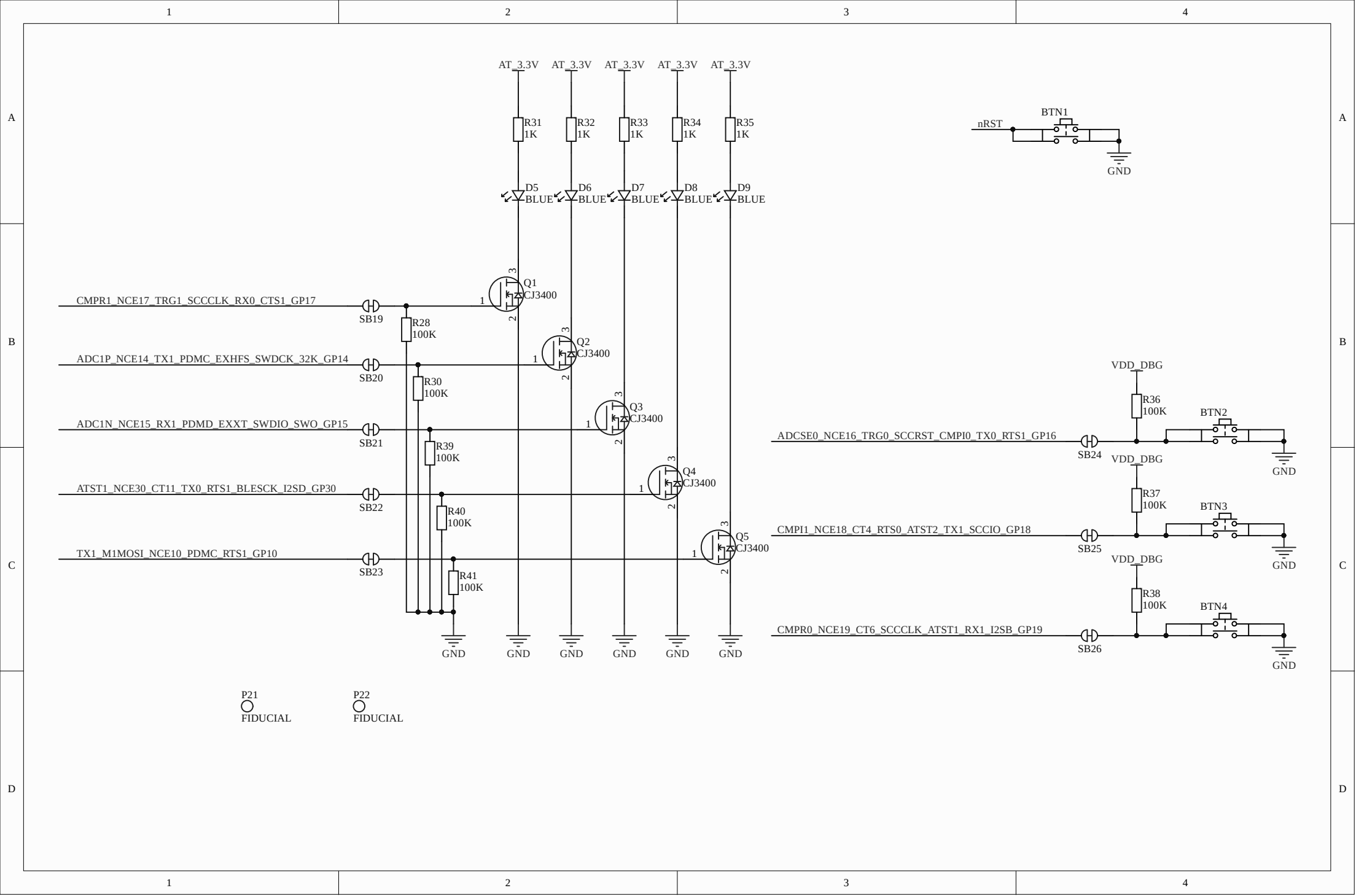
C

D

D







A

B

C

D

A

B

C

D

